



TJW

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Debabani CHOUDHURY, et al. ) Re: Request for Correction  
of Inventor Information  
)  
Serial No.: 10/716,196 ) Group: 2835  
Filing Date: November 17, 2003 ) Examiner: unknown  
For: "INTEGRATED CIRCUIT HOUSING" ) Our Ref.: B-5198NP 621388-9  
)) Date: June 2, 2005

MAIL STOP PGPUB  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The Applicants note that the publication of the above-identified application, U.S. Publication No. 2005/0082652 A1, contains an error in the address of the first-listed inventor. A copy of the front page of the publication is enclosed herewith for the Examiner's convenience.

The first-listed inventor's address and citizenship are listed on the published application as Thousand Oaks (IN). Please correct the inventor information as follows:

Debabani Choudhury, Thousand Oaks, CA (IN).

The Applicants respectfully request that the USPTO update their records accordingly.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to the "Mail Stop PGPUB, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" on June 2, 2005 by Suzanne Johnston.

Respectfully submitted,

Richard P. Berg  
Attorney for Applicant  
Reg. No. 28,145  
LADAS & PARRY  
5670 Wilshire Boulevard  
Suite 2100  
Los Angeles, CA 90036  
(323) 934-2300

Enclosures: copy of first page of published application



US 20050082652A1

(19) United States

(12) Patent Application Publication

Choudhury et al.

(10) Pub. No.: US 2005/0082652 A1

(43) Pub. Date: Apr. 21, 2005

(54) INTEGRATED CIRCUIT HOUSING

Publication Classification

- (76) Inventors: **Debabani Choudhury**, Thousand Oaks, CA (IN); **Ross Bowen**, West Hills, CA (US); **James Foshaar**, Thousand Oaks, CA (US)
- (51) Int. Cl.<sup>7</sup> ..... H01L 23/02  
(52) U.S. Cl. ..... 257/678

Correspondence Address:

LADAS & PARRY

5670 WILSHIRE BOULEVARD, SUITE 2100  
LOS ANGELES, CA 90036-5679 (US)

(57)

ABSTRACT

(21) Appl. No.: 10/716,196

(22) Filed: Nov. 17, 2003

Related U.S. Application Data

(60) Provisional application No. 60/512,014, filed on Oct. 15, 2003.

A housing for an integrated circuit, comprising a base for securing a substrate with an integrated circuit thereon, a top cover, and a body with a cavity for receiving the substrate and at least a portion of the top cover therein to form an enclosed housing therewith, the body including at least one connector extending from within the cavity to outside of the body and configured to contact the integrated circuit when the substrate is in the cavity.

